Rec'd PCT/PTO 22 FEB 2005 #2

P[illegible]EP 03/50362

10/525182

Europäisches Patentamt | **European Patent Office** | **Office européen des brevets**

REC'D 26 SEP 2003	
WIPO	PCT

Bescheinigung Certificate Attestation

Die angehefteten Unterlagen stimmen mit der ursprünglich eingereichten Fassung der auf dem nächsten Blatt bezeichneten europäischen Patentanmeldung überein.

The attached documents are exact copies of the European patent application described on the following page, as originally filed.

Les documents fixés à cette attestation sont conformes à la version initialement déposée de la demande de brevet européen spécifiée à la page suivante.

Patentanmeldung Nr. Patent application No. Demande de brevet n°

02292091.2

PRIORITY DOCUMENT

SUBMITTED OR TRANSMITTED IN COMPLIANCE WITH RULE 17.1(a) OR (b)

Der Präsident des Europäischen Patentamts;
Im Auftrag

For the President of the European Patent Office

Le Président de l'Office européen des brevets
p.o.

R C van Dijk

Anmeldung Nr:
Application no.: 02292091.2
Demande no:

Anmeldetag:
Date of filing: 23.08.02
Date de dépôt:

Anmelder/Applicant(s)/Demandeur(s):

Deutsche Thomson Brandt
Hermann Schwer Strasse 3
78048 Villingen
ALLEMAGNE

Bezeichnung der Erfindung/Title of the invention/Titre de l'invention:
(Falls die Bezeichnung der Erfindung nicht angegeben ist, siehe Beschreibung.
If no title is shown please refer to the description.
Si aucun titre n'est indiqué se referer à la description.)

Adaptive noise reduction for digital display panels

In Anspruch genommene Prioriät(en) / Priority(ies) claimed /Priorité(s) revendiquée(s)
Staat/Tag/Aktenzeichen/State/Date/File no./Pays/Date/Numéro de dépôt:

Internationale Patentklassifikation/International Patent Classification/
Classification internationale des brevets:

G09G3/00

Am Anmeldetag benannte Vertragstaaten/Contracting states designated at date of filing/Etats contractants désignées lors du dépôt:

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

ADAPTIVE NOISE REDUCTION FOR DIGITAL DISPLAY PANELS

The present invention relates to a method and device for reducing noise caused by quantization during the signal processing of a digital display device, wherein a signal charged with noise is digitally filtered with a digital filter having a plurality of filter coefficients.

Background

A PDP for Plasma Display Panel utilizes a matrix array of discharge cells, which can only be "ON", or "OFF". Therefore, it can be defined as a pure digital display. Also unlike a CRT (Cathode Ray Tube) or LCD (Liquid Crystal Display) in which gray levels are expressed by analog control of the light emission, a PDP controls the gray level by modulating the number of light pulses per frame (sustain pulses). This time-modulation will be integrated by the eye over a period corresponding to the eye time response. Since the amplitude video is portrayed by the number of light pulses, occurring at a given frequency, more amplitude means more light pulses and thus more "ON" time. For this reason, this kind of modulation is also known as PWM, pulse width modulation.

This PWM is responsible for one of the PDP image quality problems: the overall noise level, especially in the darker regions of the picture. This is due to the fact that displayed luminance is linear to the number of pulses, but the eye response and sensitivity to noise is not linear. In darker areas the eye is more sensitive than in brighter areas. This means that even though modern PDPs can display ca. 255 discrete video levels, quantization error will be quite noticeable in the darker areas. Moreover, all video pictures are pre-corrected to compensate the traditional gamma curves from standard display (e.g. CRT). Since, the plasma display is a pure linear display and does not

provide such a non-linear gamma behavior, an artificial gamma function should be performed at the display level and in a digital form. This gamma function increases the quantization steps in the dark areas whereas the quantization steps will be reduced in luminous areas. In addition, an increasing of the quantization step will drastically increase the level of the noise present in the picture.

In the following, the quantization noise after gammatization of a video signal is described.

The method used to render video levels on a plasma (PWM) is responsible for one of the PDP image quality problems: the big quantization steps, especially in the darker regions of the picture increase strongly the noise level in those areas. This is due to the fact, that displayed luminance is linear to the number of impulses for driving the luminous elements, but the eye response and sensitivity to noise is not linear. In darker areas the eye is more sensitive than in brighter areas. This means than even though modern PDPs can display ca 255 discrete video levels, quantization error will be quite noticeable in the darker areas.

Moreover, all video pictures are pre-corrected by a γ^{-1} function to compensate the traditional gamma curves (γ) from standard display (e.g. CRT). Since, the plasma display is a pure linear display and does not provide such a non-linear gamma behavior, an artificial gamma function should be applied to the display level and in a digital form. This degamma function increases the quantization noise in the dark areas whereas the quantization noise will be reduced in luminous areas.

A standard gamma function applied on 8-bit level using the following formula: $Out(x,y) = 255 \cdot \left(\frac{In(x,y)}{255}\right)^{\gamma}$ with γ ≈2 shall be taken as example. Figure 1 illustrates such a function. It shows that the gamma function applied to 8-bit level generates a strong quantization effect in the dark area. For instance, all input levels below 12 are set together to 0 after the gammatization, i.e. the application of the γ function. The following table presents the detail of the computation for some video levels:

Input (8-bit)	Output (float)	Output (8-bit)
0	0	0
1	0,003921569	0
2	0,015686275	0
3	0,035294118	0
4	0,062745098	0
5	0,098039216	0
6	0,141176471	0
7	0,192156863	0
8	0,250980392	0
9	0,317647059	0
10	0,392156863	0
11	0,474509804	0
12	0,564705882	1
13	0,662745098	1
14	0,768627451	1
15	0,882352941	1
16	1,003921569	1
17	1,133333333	1
18	1,270588235	1
19	1,415686275	1
20	1,568627451	2
21	1,729411765	2
22	1,898039216	2
23	2,074509804	2
...	...	...
250	245,0980392	245
251	247,0627451	247
252	249,0352941	249
253	251,0156863	251
254	253,0039216	253
255	255	255

This table shows that, in the dark areas, there are less output values than input values which means that the quantization steps have increased. On the opposite, in high levels, there are less input than output values (e.g. no input to generate the value 246) which means that the quantization noise has been reduced.

Standard digital pictures suffer from quantization noise which depends on the number of bits used for the digitalization. In addition to that, all natural sequences contain some natural noise (mainly gaussian noise). The overall visibility of these noise effects also depends on its temporal variation which generates a kind of bustling effect.

Figure 2 presents the video values of a standard digital video picture before gammatization. It shows an example of quantization noise and natural noise for the three color-components R,G,B of a part of the picture. This noise is enhanced by its temporal variation.

Now, there shall be given an estimation of the effect obtained on a CRT disposing of an analog gammatization function (tube behavior). For that estimation, the assumption is taken that the luminance of the white will be 100 cd/m² and that the CRT behavior can be represented by: $CRT(x,y) = 100 \cdot \left(\frac{In(x,y)}{255}\right)^{\gamma}$ with γ =2. In that case, the noise pattern on the CRT will be transformed as shown in Figure 3. From the luminance values of the three patterns R,G,B, is calculated for each component R,G,B a mean noise value and a mean error value on a CRT screen.

This shall be compared with the noise generated in the case of a plasma display. First, the gammatization will be performed at digital level (8-bit) as shown in Figure 4. The degammatization is performed on the input values as

those given in Figure 2 for the three components R,G,B. At the output a digital value is obtained.

Then, for each digital value, a luminance value can be computed taking the assumption that the plasma is a pure linear system, the value 255 is matched with 100 cd/m^2 The visibility of the noise structure can be estimated as shown in Figure 5 which corresponds to Figure 3 but in the case of a PDP.

The estimation of the noise structure on a plasma showed that the increased quantization step in the dark areas lead to a strong noise pattern. Therefore the bustling effect of the noise will increase strongly on a plasma screen in comparison to standard displays (the mean error may be up to 80%). This is also enhanced by the fact that the human visual system behavior follows a logarithm law, more sensitive for low-levels than for high levels.

As explained in the previous paragraph, the noise is more visible on a plasma than on other display in the dark areas (e.g. CRTs). Therefore, it is judicious to implement a kind of noise reduction algorithm on PDPs. Actually, various displays already dispose of such algorithms. Nevertheless, standard noise reduction algorithms also have drawbacks like a loss of sharpness, moving artifacts (trail behind strong edges).

Invention

In view of that, it is the object of the present invention to provide a method and a device for reducing the noise in an improved manner.

According to the present invention this object is solved by a method for reducing noise caused by a quantization procedure during the signal processing of a digital display device by digitally filtering a signal charged

with said noise with a digital filter having a plurality of filter coefficients, and varying at least one of said filter coefficients in dependence on a value of said signal to be filtered.

Furthermore, the above mentioned object is solved by a device for reducing noise caused by a quantisation during the signal processing of a digital display device including digital filter means for digitally filtering a signal charged with said noise, said filter means having a plurality of filter coefficients, and controlling means connected to said digital filter means for varying at least one of said filter coefficients in dependence on a value of said signal to be filtered.

Further favourable developments of the present invention are set out in the subclaims.

Advantageously, there may be provided a noise reduction algorithm which has an effect decreasing with the video level, so that a maximum filtering is applied for low-levels (critical noisy regions) whereas no filtering or very low filtering is applied for luminous regions (less noise, more critical to noise reduction algorithms). Such an adaptive noise filter may be applied after the gammatization process of the plasma. The adaptive filtering is a specific filtering which suits to the gammatization quantization noise. In other words, the filtering will be maximum for dark areas and its efficacy will automatically decrease when the luminance of the area is increasing.

The application of the filtering according to the present invention leads to the following advantages:

- The noise on a plasma panel is reduced in its critical regions.
- The sharpness of the picture is not reduced or details do not disappear.
- Moving artefacts do not appear.

Drawings

Exemplary embodiments of the invention are illustrated in the drawings and are explained in more detail in the following description. The drawings showing in:

Figure 1 a standard gamma function to be applied to the video signal;

Figure 2 an example of quantization noise and natural noise for the three colour-components of a picture;

Figure 3 the noise pattern on a CRT disposing of an analog gammatization function;

Figure 4 a gammatization performed at a digital level of 8 bits;

Figure 5 an estimation of the visibility of the noise structure on a PDP after gammatization;

Figure 6 a filter mask applied to a current pixel;

Figure 7 a diagram showing the variation of filter parameters;

Figure 8 the structure of a two dimensional median filter;

Figure 9 an implementation of a median filter;

Figure 10 variations of median filters;

Figure 11 an implementation of an adaptive median filtering; and

Figure 12 a hardware implementation of the inventive algorithm.

In order to better understand the present concept two kind of standard noise reduction algorithms are now presented as preferred embodiments.

Low-pass filtering

The analysis shall be limited to 2-dimensional low-pass filters based on 3 pixels and three lines. Obviously such filters can be extended in the spatial dimension (more or less pixel, more or less lines) as well as in the temporal direction by applying a kind of recursivity (requires a frame memory).

In the following three standard types of low-pass filters (3x3) known in the literature are illustrated:

$$\frac{1}{9}\begin{vmatrix}1 & 1 & 1\\1 & 1 & 1\\1 & 1 & 1\end{vmatrix} \qquad \frac{1}{10}\begin{vmatrix}1 & 1 & 1\\1 & 2 & 1\\1 & 1 & 1\end{vmatrix} \qquad \frac{1}{16}\begin{vmatrix}1 & 2 & 1\\2 & 4 & 2\\1 & 2 & 1\end{vmatrix}$$

The various masks will be centered to the current pixel as shown in Figure 6 by the square surronunding the number 21. The calculation of the filtering result is also shown in the figure.More specifically, a mask of 3x3 pixels is applied on the picture centered to the current pixel. Then a convolution product is realised between the values delimited by the mask and the filter as clearly shown on said figure 6 giving the resulting values of the right pattern in figure 6.

In the case of the plasma one can develop two kinds of video adapted low-pass filtering as presented below:

$$\frac{1}{(8\cdot\alpha+1)}\begin{vmatrix}\alpha & \alpha & \alpha\\\alpha & 1 & \alpha\\\alpha & \alpha & \alpha\end{vmatrix} \qquad \frac{1}{(4\cdot(\alpha+\beta)+1)}\begin{vmatrix}\beta & \alpha & \beta\\\alpha & 1 & \alpha\\\beta & \alpha & \beta\end{vmatrix}$$

In these two kinds of PDP filtering the factors α and β will have a value decreasing with the luminance of the current pixel. Two examples of a possible variation of these parameters are shown in Figure 7.

This low-pass filtering is already well adapted to PDP requirements except for the fact that some disturbances can be generated on sharp transition. The case of a current dark pixel located near to a white element shall be taken as example. In that case, this white element will be used for the low-pass filtering which is not the objective. Therefore, more adaptation should be added to the filtering as described below.

For the future explanation the current pixels in the screen shall be described by x_0 and the pixels around using the following definition:

$$\begin{vmatrix} x_2 & x_3 & x_4 \\ x_1 & x_0 & x_5 \\ x_8 & x_7 & x_6 \end{vmatrix}$$

Based on this assumption, a more general adapted low-pass filtering for the PDP will be defined as following:

$$\frac{1}{\sum_{l=0}^{l=8} a_l}\begin{vmatrix} a_2 & a_3 & a_4 \\ a_1 & a_0 & a_5 \\ a_8 & a_7 & a_6 \end{vmatrix}$$

with $a_0=1$ and with $a_l=f_l(x_0\,,\,x_l)$

As an example one can describe the function f_l as following:

$$f_{2n}(x_o,x_{2n})=\left|\begin{matrix}\alpha \text{ if } |x_{2n}-x_0|\le\Delta \\ 0\end{matrix}\right. \quad \text{and} \quad f_{2n+1}(x_o,x_{2n+1})=\left|\begin{matrix}\beta \text{ if } |x_{2n+1}-x_0|\le\Delta \\ 0\end{matrix}\right. \quad \text{with } \square$$

representing a limit of neighbour which can be taken into account by the filtering. This solution is well adapted in case of big difference of values between two adjacent pixels.

Median filtering

At the beginning of the present analysis, the filters have been limited to 2-dimensional low-pass filters based on 3 pixels and three lines. Obviously such filters can be extended in the spatial dimension (more or less pixels, more or less lines) as well as in the temporal direction (requires a frame memory).

The median filter selects, in an analysis window, the pixel having the median value. For that purpose, the analysis window contains an odd number of pixels that will be ordered. Then, the new computed value will be the value having the median position. An example of a median filter 3x3 is shown in Figure 8. It may be formulated as follows:

$$med\left(\begin{vmatrix} x_2 & x_3 & x_4 \\ x_1 & x_0 & x_5 \\ x_8 & x_7 & x_6 \end{vmatrix}\right) = med(x_0, x_1, x_2, x_3, x_4, x_5, x_6, x_7, x_8)$$

Figure 9 presents a way to simply implement a median filter based on simple function (comparators) like MIN() and MAX().

Other median filters can be used like a filter max/median which can be defined as illustrated in Figure 10. These functions realize a maximum or a median of three medians having various analysis directions.

In any case, it has to be said that a median filter having a size of 2N+1 pixels suppress in the picture all details having a size smaller or equal to N.

Therefore, in the case of the PDP adaptive median filtering, one can use various filters depending on the value of the current pixels. Figure 11 presents a possible implementation of such an adaptive filtering, the choice of the filters depending on the video level. It represents only an example of an adaptive median filtering implemented **after** the gammatization process in the PDP.

General filtering

As already said, the main idea is to use a noise reduction algorithm which has a decreasing effect when the video level of the current pixel is increasing. Moreover, the filtering will be applied **after** the gammatization process which can be made on more than 8 bits because of further operations like dithering. Obviously, an operation like a dithering should be made after the noise reduction in order not to be deactivated by the noise reduction itself.

Algorithm implementation:

Figure 12 illustrates a possible hardware implementation for the algorithm.

RGB input pictures are forwarded to the gamma function block: this can include a LUT or a mathematical function. The outputs of this block (8-bit or more) are forwarded to the noise reduction block. This last block, depending on the current value of a pixel, will apply various noise reduction filters at the same bit resolution. Then, the output is forwarded to the dithering block which applies differents kinds of dithering (e.g. such as described for example, in EP-A-1136974, EP-01250199.5 and EP-02291924.5 in the name of the present Applicant). The further signal processing is performed as usual by a subsequent sub-field coding block, a serial/parallel converter, a parallel acting plasma controller and final PDP.

As already set out above, the main idea is to have a maximum of noise reduction for dark areas where the noise is really disturbing (eye sensitivity stronger, gammatization critical) and where the information in terms of detail is less relevant. On the other hand, the level of the filtering will decrease together with the luminance up to no filtering for high luminance levels where the noise is less disturbing (no effect of quantization, less eye sensitivity) but where the information in terms of details will be the more relevant.

Claims

1. Method for reducing noise caused by a quantization procedure during the signal processing of a digital display device by digitally filtering a signal charged with said noise with a digital filter having a plurality of filter coefficients,
characterized by
varying at least one of said filter coefficients in dependence on a value of said signal to be filtered.

2. Method according to claim 1, wherein said signal includes a matrix of video levels of pixels of said display device.

3. Method according to claim 1 or 2, wherein said value of said signal includes a video level of a current pixel.

4. Method according to one of the claims 1 to 3, wherein said filtering includes one and/or two dimensional low pass filtering.

5. Method according to one of the claims 1 to 4, wherein said filtering includes one and/or two dimensional median filtering.

6. Method according to one of the claims 1 to 5, wherein the value of a filter coefficient decreases with a luminance of a current pixel.

7. Method according to one of the claims 1 to 6, wherein the structure of said digital filter varies with the video level of a current pixel.

8. Method according to one of claims 1 to 7, wherein, in case of a low pass filter, the coefficients are given by $\frac{1}{\sum_{i=0}^{i=8} a_i} \begin{vmatrix} a_2 & a_3 & a_4 \\ a_1 & a_0 & a_5 \\ a_8 & a_7 & a_6 \end{vmatrix}$

with $a_0=1$ and with $a_i=f_i(x_o, x_i)$.

9. Method according to claim 8, wherein, the function is the following:

$$f_{2n}(x_o, x_{2n}) = \left| \begin{matrix} \alpha \text{ if } |x_{2n} - x_0| \le \Delta \\ 0 \end{matrix} \right. \quad \text{and} \quad f_{2n+1}(x_o, x_{2n+1}) = \left| \begin{matrix} \beta \text{ if } |x_{2n+1} - x_0| \le \Delta \\ 0 \end{matrix} \right.$$

with Δ a limit of neighbor.

10. Device for reducing noise caused by a quantisation during the signal processing of a digital display device including
digital filter means for digitally filtering a signal charged with said noise, said filter means having a plurality of filter coefficients,
characterized by
controlling means connected to said digital filter means for varying at least one of said filter coefficients in dependence on a value of said signal to be filtered.

11. Device according to claim 10, wherein said signal includes a matrix of video levels of pixels of said display device.

12. Device according to claim 10 or 11, wherein said value of said signal includes a video level of a current pixel.

13. Device according to one of the claims 10 to 12, wherein said digital filter means includes one and/or two dimensional low pass filter.

14. Device according to one of the claims 10 to 13, wherein said digital filter means includes a one and/or two dimensional median filter.

15. Device according to one of the claims 10 to 14, wherein the value of a filter coefficient is decreasable with the luminance of a current pixel by said controlling means.

16. Device according to one of the claims 10 to 15, wherein the structure of a filter of said digital filter means is variable with the video level of a current pixel by said controlling means.

Abstract

Adaptive noise reduction for digital display panels

A plasma display panel is a pure linear display and does not provide a non-linear gamma behaviour like a CRT so that an artificial gamma function has to be applied to the signal in digital form. This gamma function increases the quantization steps in the dark areas whereas the quantization steps will be reduced in the luminous areas. The basic idea is to apply an adaptive noise filtering after the gammatization process. The adaptive filtering is a specific filtering which is adapted to the gammatization quantization noise. In other words, the filtering will be maximum for dark areas and its efficacy will be automatically decreased when the luminance of the area is increasing.

(Figure 12)

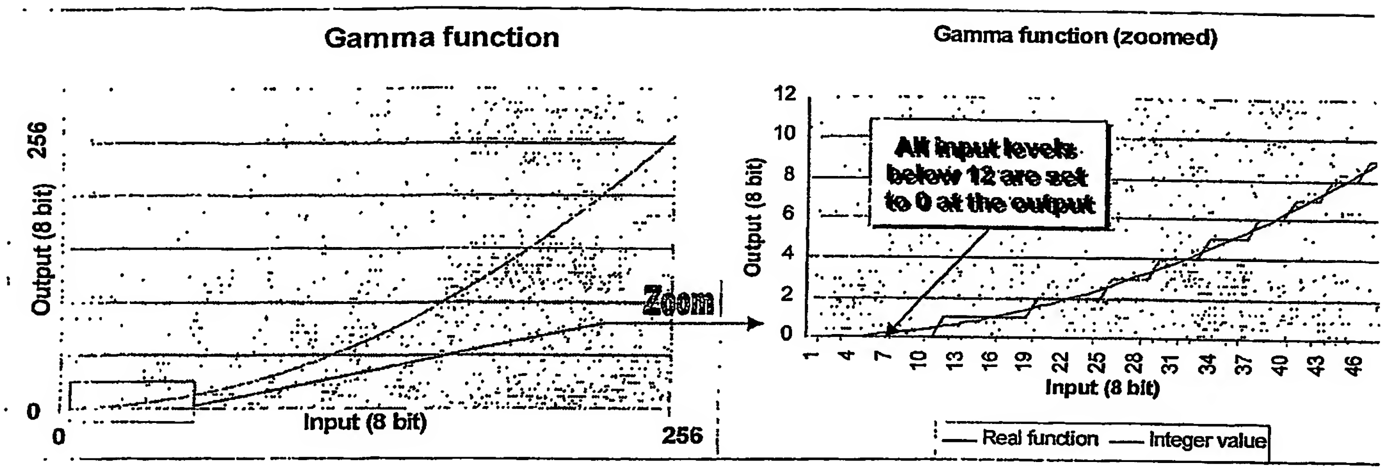
Gamma function
Output (8 bit)
256
0
Input (8 bit)
0
256
Zoom
Gamma function (zoomed)
12
10
8
6
4
2
0
Output (8 bit)
All input levels below 12 are set to 0 at the output
1 4 7 10 13 16 19 22 25 28 31 34 37 40 43 46
Input (8 bit)
Real function
Integer value

Fig. 1

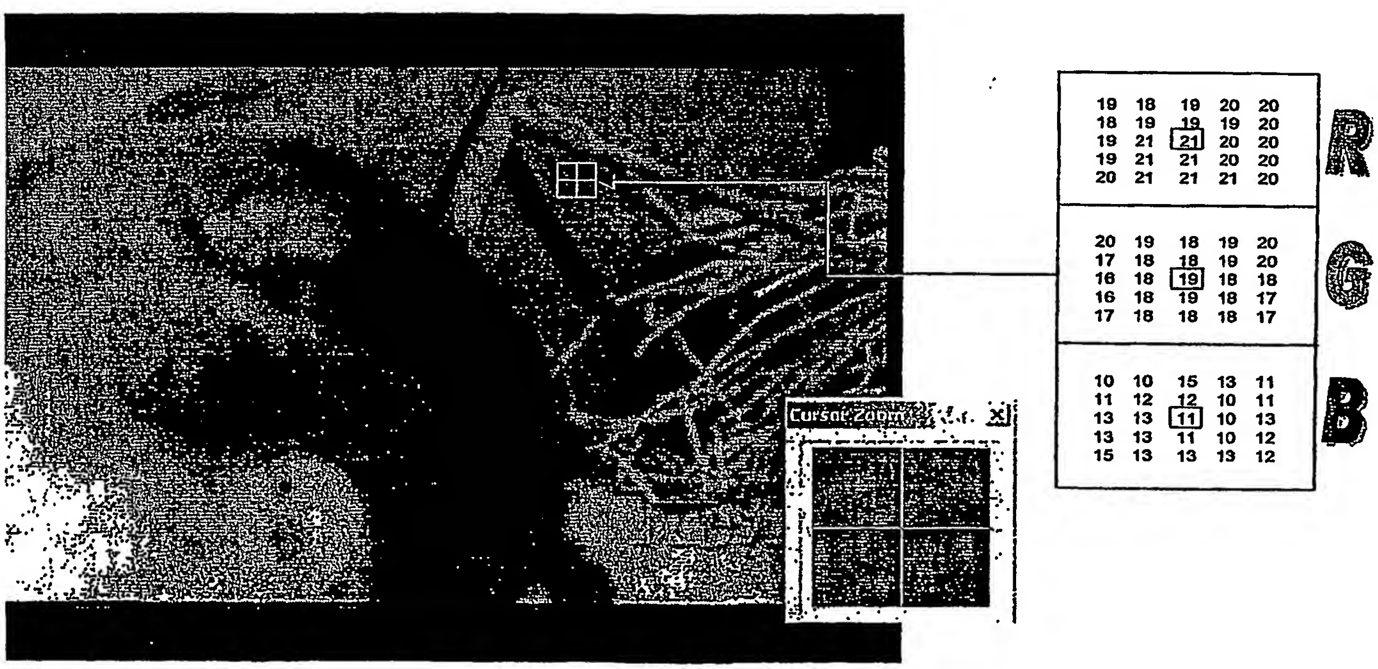
Cursor Zoom
R
19 18 19 20 20
18 19 19 19 20
19 21 21 20 20
19 21 21 20 20
20 21 21 21 20
G
20 19 18 19 20
17 18 18 19 20
16 18 19 18 18
16 18 19 18 17
17 18 18 18 17
B
10 10 15 13 11
11 12 12 10 11
13 13 11 10 13
13 13 11 10 12
15 13 13 13 12

Fig. 2

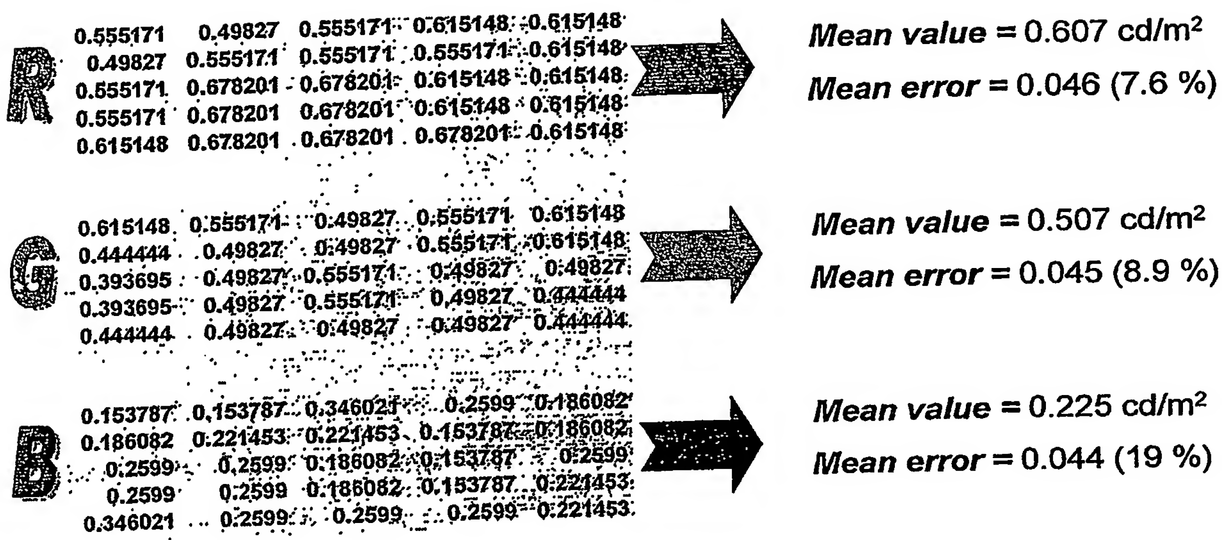
R
0.555171 0.49827 0.555171 0.615148 0.615148
0.49827 0.555171 0.555171 0.555171 0.615148
0.555171 0.678201 0.678201 0.615148 0.615148
0.555171 0.678201 0.678201 0.615148 0.615148
0.615148 0.678201 0.678201 0.678201 0.615148
Mean value = 0.607 cd/m2
Mean error = 0.046 (7.6 %)
G
0.615148 0.555171 0.49827 0.555171 0.615148
0.444444 0.49827 0.49827 0.555171 0.615148
0.393695 0.49827 0.555171 0.49827 0.49827
0.393695 0.49827 0.555171 0.49827 0.444444
0.444444 0.49827 0.49827 0.49827 0.444444
Mean value = 0.507 cd/m2
Mean error = 0.045 (8.9 %)
B
0.153787 0.153787 0.346021 0.2599 0.186082
0.186082 0.221453 0.221453 0.153787 0.186082
0.2599 0.2599 0.186082 0.153787 0.2599
0.2599 0.2599 0.186082 0.153787 0.221453
0.346021 0.2599 0.2599 0.2599 0.221453
Mean value = 0.225 cd/m2
Mean error = 0.044 (19 %)

Fig. 3

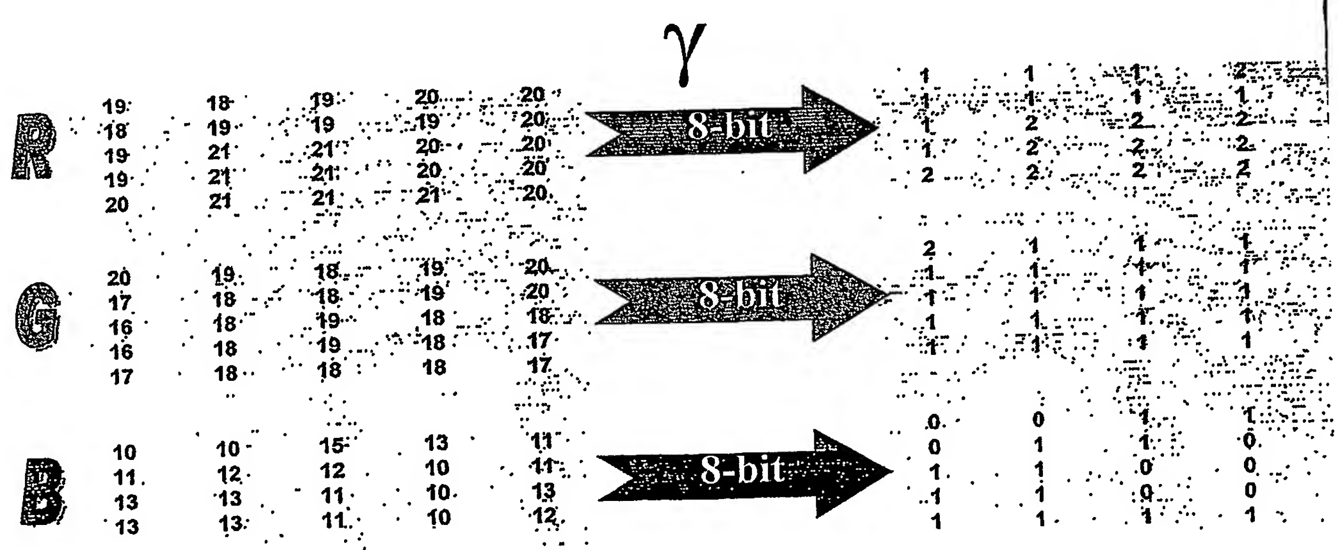
γ
R
19 18 19 20 20
18 19 19 19 20
19 21 21 20 20
19 21 21 20 20
20 21 21 21 20
8-bit
G
20 19 18 19 20
17 18 18 19 20
16 18 19 18 18
16 18 19 18 17
17 18 18 18 17
8-bit
B
10 10 15 13 11
11 12 12 10 11
13 13 11 10 13
13 13 11 10 12
8-bit
0 0 1 1
0 1 1 0
1 1 0 0
1 1 0 0
1 1 1 1

Fig. 4

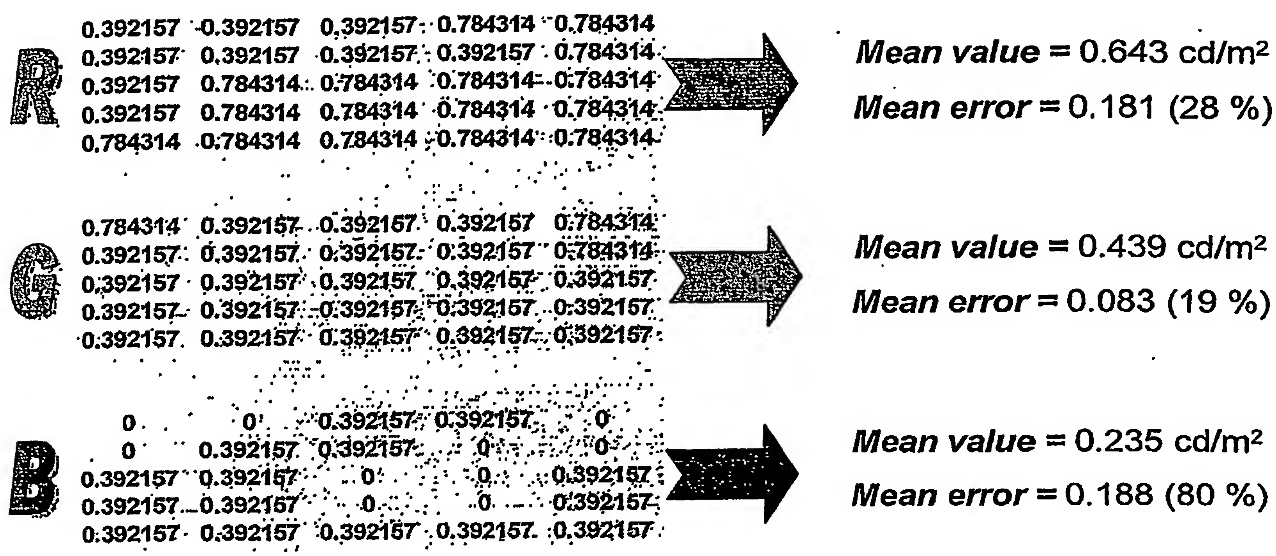
R
0.392157 0.392157 0.392157 0.784314 0.784314
0.392157 0.392157 0.392157 0.392157 0.784314
0.392157 0.784314 0.784314 0.784314 0.784314
0.392157 0.784314 0.784314 0.784314 0.784314
0.784314 0.784314 0.784314 0.784314 0.784314
Mean value = 0.643 cd/m²
Mean error = 0.181 (28 %)
G
0.784314 0.392157 0.392157 0.392157 0.784314
0.392157 0.392157 0.392157 0.392157 0.784314
0.392157 0.392157 0.392157 0.392157 0.392157
0.392157 0.392157 0.392157 0.392157 0.392157
0.392157 0.392157 0.392157 0.392157 0.392157
Mean value = 0.439 cd/m²
Mean error = 0.083 (19 %)
B
0 0 0.392157 0.392157 0
0 0.392157 0.392157 0 0
0.392157 0.392157 0 0 0.392157
0.392157 0.392157 0 0 0.392157
0.392157 0.392157 0.392157 0.392157 0.392157
Mean value = 0.235 cd/m²
Mean error = 0.188 (80 %)

Fig. 5

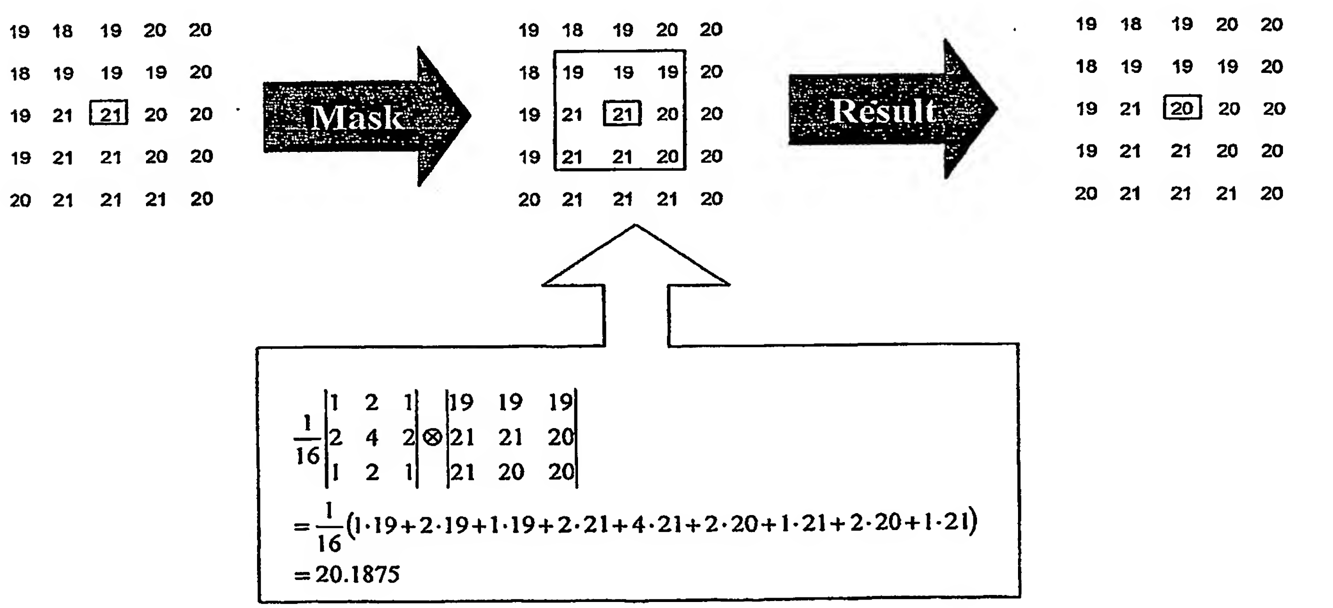
19 18 19 20 20
18 19 19 19 20
19 21 21 20 20
19 21 21 20 20
20 21 21 21 20
Mask
19 18 19 20 20
18 19 19 19 20
19 21 21 20 20
19 21 21 20 20
20 21 21 21 20
Result
19 18 19 20 20
18 19 19 19 20
19 21 20 20 20
19 21 21 20 20
20 21 21 21 20
$\frac{1}{16}\begin{vmatrix}1 & 2 & 1\\ 2 & 4 & 2\\ 1 & 2 & 1\end{vmatrix} \otimes \begin{vmatrix}19 & 19 & 19\\ 21 & 21 & 20\\ 21 & 20 & 20\end{vmatrix}$
$= \frac{1}{16}(1\cdot 19 + 2\cdot 19 + 1\cdot 19 + 2\cdot 21 + 4\cdot 21 + 2\cdot 20 + 1\cdot 21 + 2\cdot 20 + 1\cdot 21)$
$= 20.1875$

Fig. 6

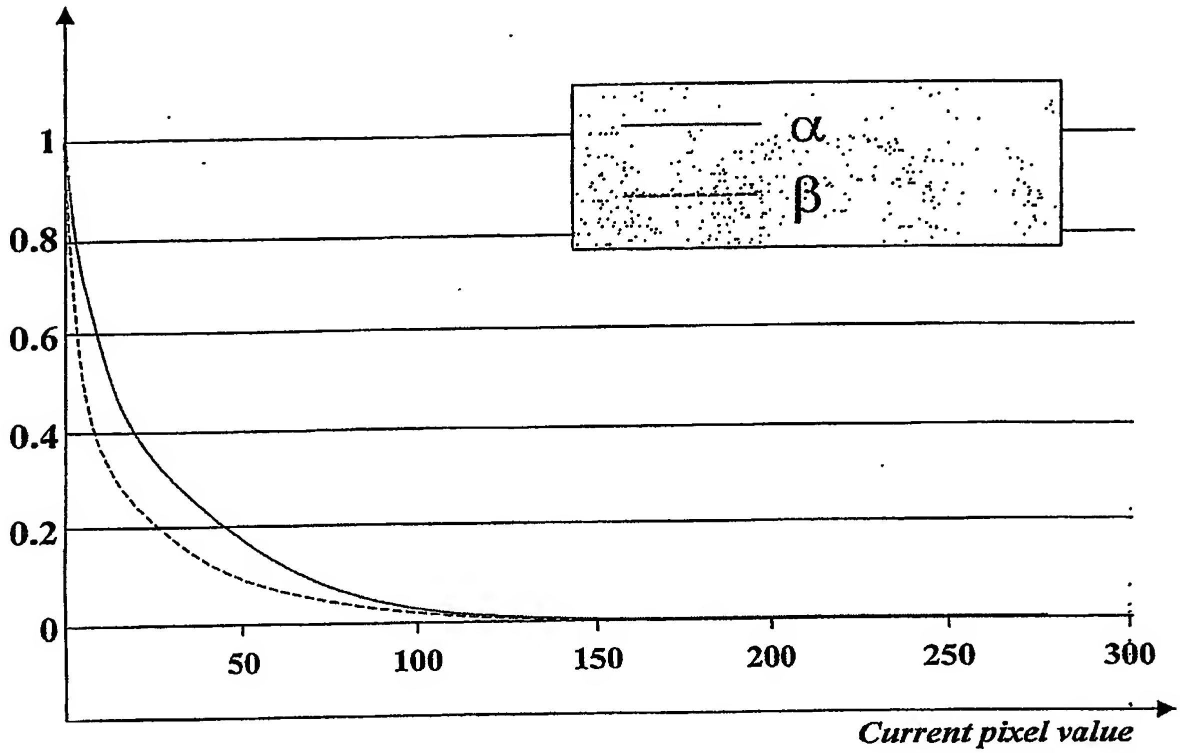

Fig. 7

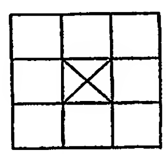

Fig. 8

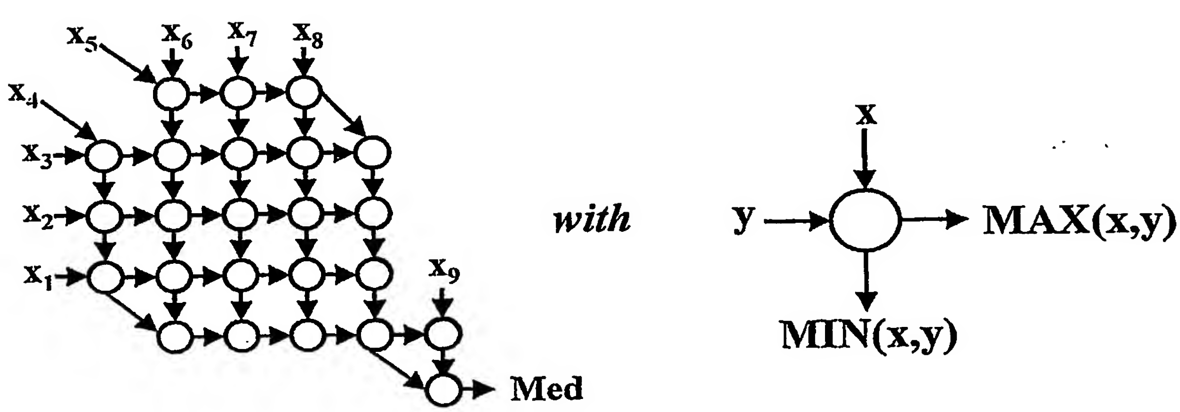

Fig. 9

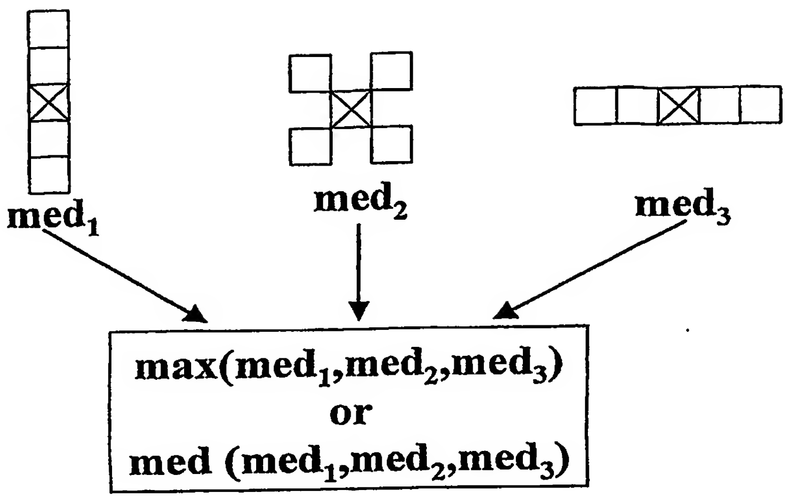
med$_1$
med$_2$
med$_3$
max(med$_1$,med$_2$,med$_3$)
or
med (med$_1$,med$_2$,med$_3$)

Fig. 10

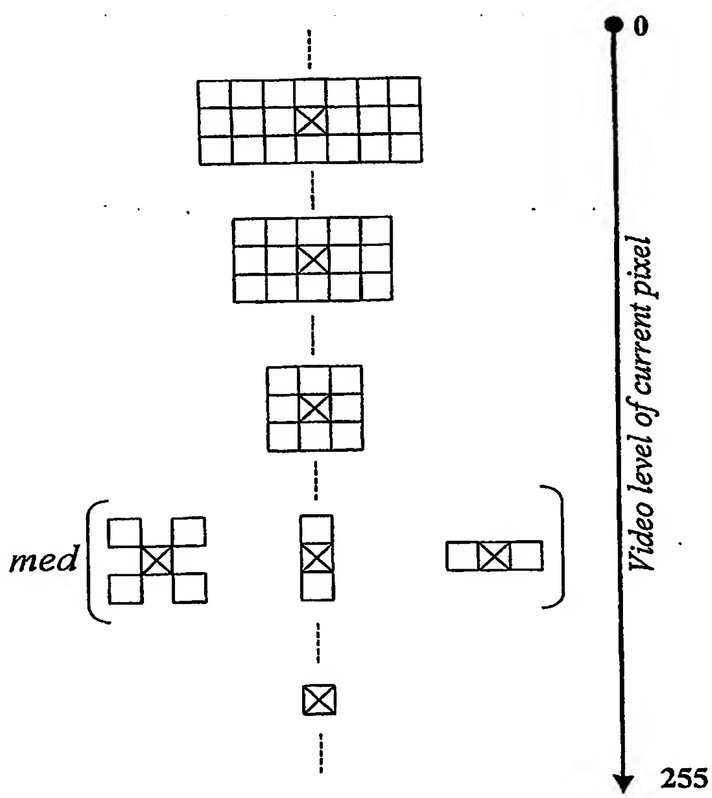
0
med
Video level of current pixel
255

Fig. 11

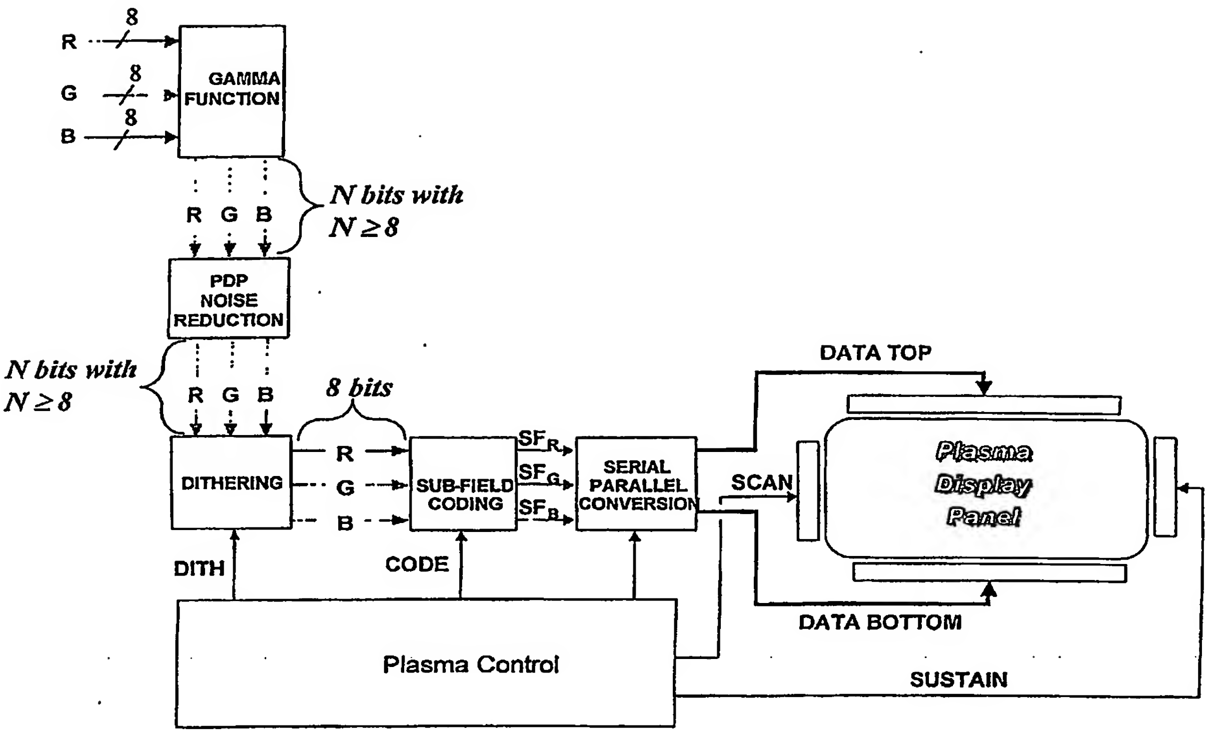
R
G
B
8
8
8
GAMMA FUNCTION
R G B
N bits with N ≥ 8
PDP NOISE REDUCTION
N bits with N ≥ 8
R G B
8 bits
DITHERING
R
G
B
SUB-FIELD CODING
SF_R
SF_G
SF_B
SERIAL PARALLEL CONVERSION
SCAN
DATA TOP
DATA BOTTOM
Plasma Display Panel
DITH
CODE
Plasma Control
SUSTAIN

Fig. 12